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Vinay M K

Objective			
00,000,00	and application of the	on in speech, audio, image or video p e same on practical real world system rs of management and technical verti	ns. To move up
Education	 1999 – 2001 Master of Technology, ➢ CPI 10 (maximum) of the second secon	Indian Institute of Technology Digital Signal Processing.	Kanpur
	· · · · · · · · · · · · · · · · · · ·	d at ICASSP -2002 and TENCON -2001 .	
	 University of Mysore Passed with an overa Secured the Chandra 	National Institute of Engineering ng, Electronics and Communications e second rank holder among more than 300 all (for all the eight semesters) aggregate of ashekar memorial award for being the higher students of all branches for all the four seme	83%. st average scorer in
Summary of Experience	codes (on DSP and H		G-1, MPEG-2 &
	-	FASET, Dept of CSE, IITK Fory, lab & projects), as a part of summer volubout 30 for one and a half months.	Kanpur ocational job.
	 1999 – Feb 2001 Teaching Assistant Worked as one of the scholarship for the m 	Dept of Electrical Engineering, IITK e departmental web page developers, under nasters program.	
	1998 - 1999Software EngineerWorked as a trainee	Infosys Technologies Limited on BSD-UNIX platform and retailing busin	Bangalore ess domain.

Title	GSM AMR (Narrow band) codec reference code development
Group Size	2
My Role	Interface design, memory optimization (both stack as well as heap), reduction in code size, reduce function calls & provide support for optimization team.
Technical details	 Started with fixed point version available from ETSI – 3GPP site Develop a re-entrant code/library Reduce un-necessary heap variables Optimize codebook search and other critical functions

Title	GSM AMR (Narrow band) codec floating point to fixed point conversion
Group Size	2
My Role	Conceptual design, writing fixed point versions of math functions like log, pow2, exp, sqrt etc Design of data flow to optimize computation and data flow complexities.
Technical details	 Estimating formats of various intermediate stages and design data path for efficient usage of precision and computing power. Avoid re-computations by optimal storage. Design of block exponent algorithms for efficient codebook search.

Title	Error concealment solution for celp based encoded speech transmission on IP
Group Size	2
My Role	Conceptual design, conceiving a suitable algorithm matching our interface, memory, computation power, delay constraints and minimum quality thresholds.
Technical details	 Quantifying the steps to be incorporated in our error concealment solution. Solely decoder based, consistent interface independent of channel error resilient techniques were the initial constraints for algorithm design. Suitable and efficient parameter estimation based on phoneme region. <i>Finer technical details of the project cannot be disclosed as it is the propriety of Emuzed Inc.,But an overview of the same can be obtained by [1] in publication list above.</i>

Title	Optimisation of GSM AMR NB codec on ADI Blackfin family of processors
Group Size	3
My Role	Design of constant table usage avoiding extern variables.
Technical details	 Xdias a propriety vendor-application interface framework as developed by Texas Instruments was used. Redesign of the algorithms to make efficient usage of dual MAC, vector operations and byte operations. Optimisation of address calculations by employing local pointers Effective usage of hardware loops and redesign of code to avoid stalls.

Title	<i>MPEG-1 audio layer 1 & 2 encoder reference code development & floating to fixed-point conversion</i>
Group Size	3
My Role	C level optimization of modules like rate control, psycho-acoustic model quantization etc., Implementing various math operations in fixed point

	arithmetic. Estimating optimal data formats based on audio quality improvement.
Technical details	 Reduction of complexity by reusing various parameters like energies, filter bank values etc., Implementing psycho-acoustic model 1 & 2 in fixed-point arithmetic with fine-tuning for good audio quality.

Title	MPEG –2 & 4 advanced audio codec development
Group Size	3
My Role	Implemented stereo processing, TNS, psycho-acoustic, block switching modules in the encoder. Adding features like frequency transformations, stereo to mono and vice versa, etc on the decoder.
Technical details	 Encoder implementation involved adding stereo processing block, block switching, temporal noise shaping & psycho acoustic modules. Fine tuning the psycho-acoustic model for improving the audio quality. Optimization at design level for reduction in computational complexity. Decoder implementation involved adding features like sampling frequency conversions for an already existing decoder library. Solving tricky issues related to AAC_LTP object, stereo to mono conversion and back.

Title	Optimisation of MPEG –2 advanced audio encoder on ARM9 of DM310 chip
Group Size	1
My Role	Optimization of AAC encoder on ARM9TDMI
Technical details	 Implemented audio IO via mcBSP serial port and AIC-23 on DM310. The encoder is implemented on ARM sub system side of the SOC. C level optimization

Title	Float to fixed point conversion of TIA/EIA speech codec
Group Size	2
My Role	Providing technical guidance for float to fixed point conversion of the celp based 14.4kbps speech codec.

Mtech Thesis	Nonlinear frequency warping in speaker normalization
	• Application of various warping factor functions in speech recognition problem. A comparative study of warping methods with scale invariance transforms, in speaker normalization efforts.
	• Frequency domain uniform and non-uniform warping implementation in ML- sense on a HMM based continuous-speech digit recognizer
	• Uses HTK toolkit and also an independent implementation using C++ modules adapted from RES software.
	• A simple normalization method proposed based on the work was accepted at TENCON-2001.
	• Along with more adaptation and extra effort by two more students the complete work was presented as a conference paper at ICASSP-2002.

Guide Dr. S. Umesh, Asst. professor, IITK.

BE Project	 Simulation of ARINC-429 Communication Protocol. National Aerospace Laboratories, Bangalore. Abstract: The project implements a part of the larger project meant for avionics suite testing. A user friendly, interactive application layer is built (Using C++ and OOP concepts), which accepts data in engineering format and converts it to ARINC standards as per ARINC 429 conventions. Bit oriented protocol is implemented with the help of an ARINC card, to set up the communication environment. The software is tested and verified on a twin computer set up, at National aerospace laboratories (NAL), Bangalore.
	Guides : Mrs. Vijayalakshmi Shankar, Scientist NAL, and Bangalore. Dr. H. S. Shivaram, Prof and head E&C dept, NIE Mysore.
Publications	 Vinay M K and Suresh Babu P V, "Context based error recovery technique for GSM AMR speech codec", International conference on acoustic, speech signal processing (ICASSP) 2002, Orlando Florida, USA, May 13-17 2002. S Umesh, V Bharatkumar, Vinay M K, Rajesh Sharma and Rohit Sinha, "A Simple approach non-uniform vowel normalization", International conference on acoustic, speech signal processing (ICASSP) 2002, Orlando Florida, USA, May 13-17 2002. Manish Arora, Suresh Babu PV and Vinay M K, "RISC processor based speech codec implementation for emerging mobile multimedia messaging solutions", 14th International conference on digital signal processing, DSP-2002, Santorini, Greece, July 1-3 2002.

Soft copy of the thesis, above publications and other works of mine can be downloaded from my web page at http://mkvinay.tripod.com/publications.htm

References can be provided if required.